

Notice of Allowability	Application No.	Applicant(s)	
	10/531,789	ASO, SHINJI	
	Examiner	Art Unit	
	Y. J. Han	2838	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to <u>a case filed on 4/18/05</u> .			
2. The allowed claim(s) is/are <u>1-8</u> .			
 3.			
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4/18/05 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview S Paper No 8), 7. ☐ Examiner's	nformal Patent Application (PTO-152) Gummary (PTO-413), ./Mail Date s Amendment/Comment s Statement of Reasons for Allowance	

DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-9 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

Claim 1 recites, inter alia, error calculation means configured to calculate an error output between the ideal control signal generated by the control signal generation means and an actual control signal which switches on the main switch; and delay control means configured to control a delay of an ON time of the main switch by the actual control signal based on the error output of the error calculation means, thereby performing control to make the actual control signal approach the ideal control signal.

Claim 2 recites, inter alia, error calculation means configured to calculate an error output between the ideal control signal generated by the control signal generation means and an actual control signal which switches on the main switch; and delay control means configured to control a delay of an ON time of the main switch by the actual control signal based on the error output of the error calculation means, thereby performing control to make the actual control signal approach the ideal control signal.

The art of record does not disclose the above limitations, nor would it be obvious to modify the art of record so as to include either of the above limitations.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Y. J. Han whose telephone number is 571-272-2078. The examiner can normally be reached on Mon-Fri 6:30am-3:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl Easthom can be reached on 571-272-1989. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

J. Han

Primary Examiner
Art Unit 2838